

**Amendments to the Specification:**

Please amend the following paragraphs on page 1 of the specification:

**CROSS-REFERENCE TO RELATED APPLICATIONS**

This application is related to the following co-pending U.S. Patent Applications that are each incorporated by reference as if set forth in full in this application:

“ADAPTABLE DATAPATH FOR A DIGITAL PROCESSING SYSTEM,”  
Serial No. 10/626,833 ~~{TBD}~~, filed 7/23/2003 ~~{TBD}~~ (Our Ref. No. 021202-003710US);

“CACHE FOR INSTRUCTION SET ARCHITECTURE USING INDEXES  
TO ACHIEVE COMPRESSION,” Serial No. 10/628,083 ~~{TBD}~~, filed 07/24/2003 ~~{TBD}~~  
(Our Ref. No. 021202-003730US);

“ADAPTIVE INTEGRATED CIRCUITRY WITH HETEROGENEOUS  
AND RECONFIGURABLE MATRICES OF DIVERSE AND ADAPTIVE  
COMPUTATIONAL UNITS HAVING FIXED, APPLICATION SPECIFIC  
COMPUTATIONAL ELEMENTS,” Serial No. 09/815,122, filed on March 22, 2001.